MULTIPLEX CONSTRUCTION

1. Mux 1 file
2. Picture attached
3. The current from the output on the 74150 chip gives alternative output from the input.

ADDER CIRCUIT

|  |  |  |  |  |
| --- | --- | --- | --- | --- |
| A | B | Cin | Cout | Y |
| 0 | 0 | 0 | 0 | 0 |
| 0 | 0 | 1 | 1 | 0 |
| 0 | 1 | 0 | 1 | 0 |
| 0 | 1 | 1 | 0 | 1 |
| 1 | 0 | 0 | 1 | 0 |
| 1 | 0 | 1 | 0 | 1 |
| 1 | 1 | 0 | 0 | 1 |
| 1 | 1 | 1 | 1 | 1 |

1. SOP Boolean equation = (~A) B Cin + A (~B) Cin + A B (~Cin) + A B Cin
2. Optional [didn’t do it]
3. Files for it attached.